

Figure 1
programmable
logic device 10

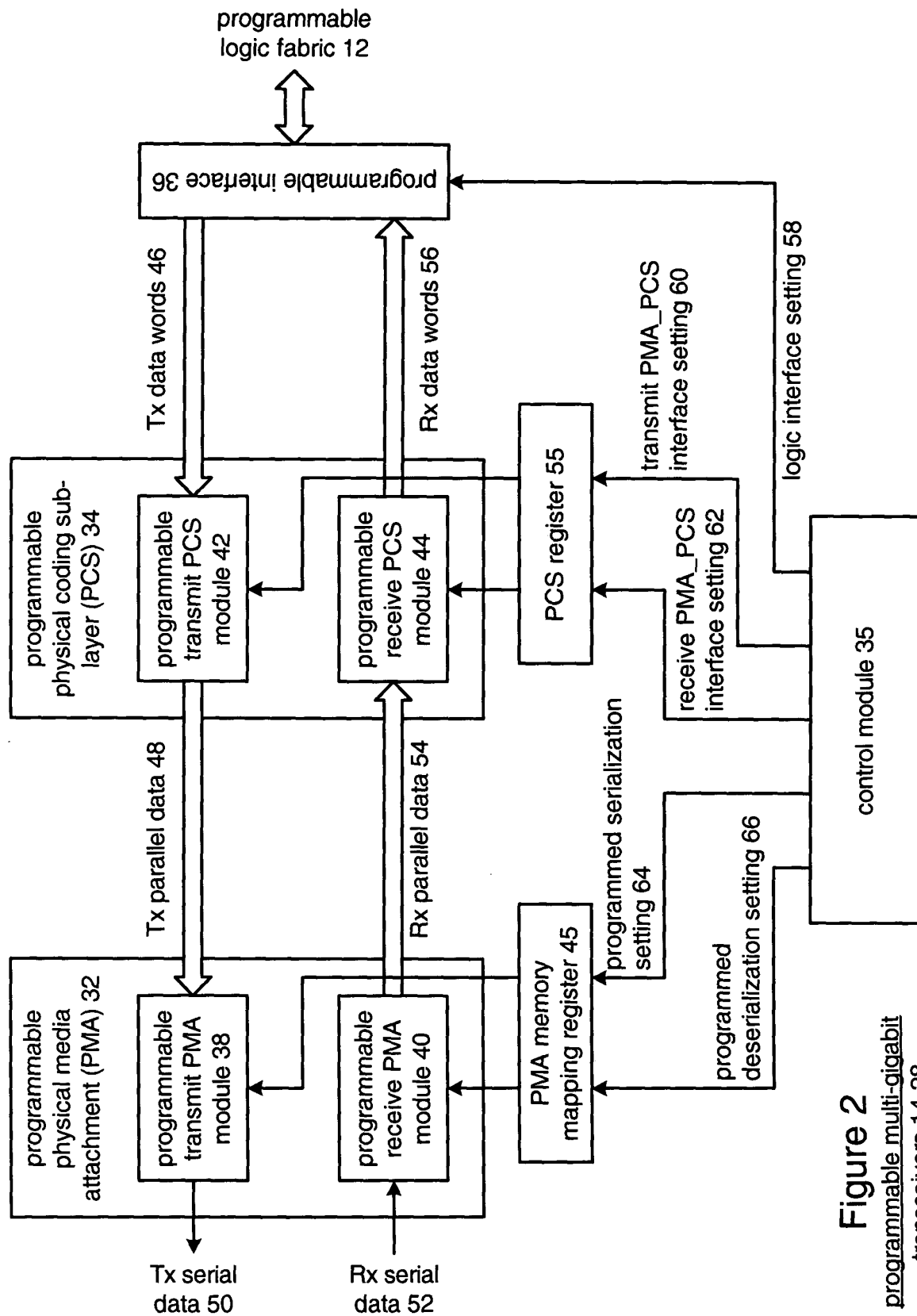


Figure 2
programmable multi-gigabit
transceivers 14-28

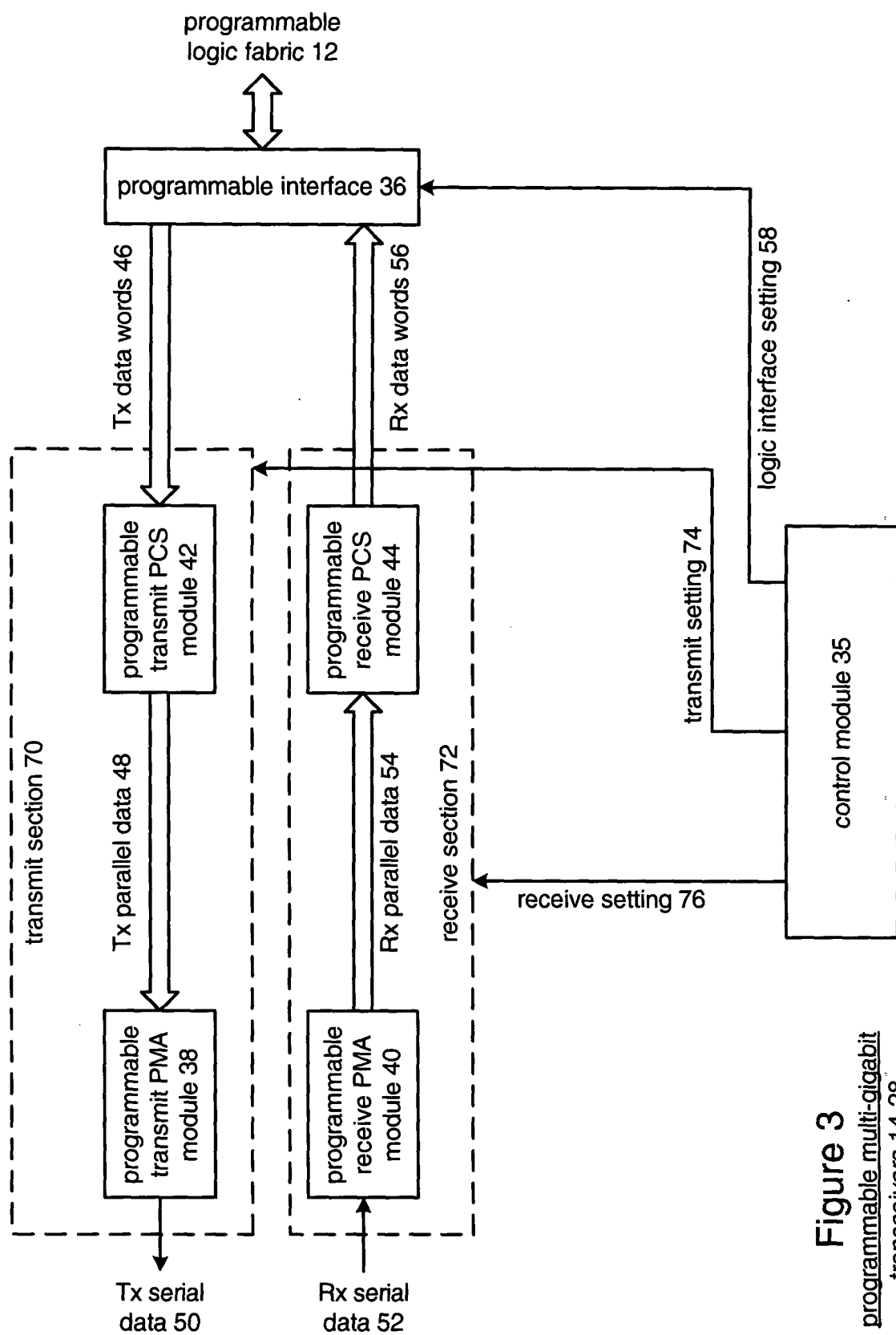


Figure 3
programmable multi-gigabit
transceivers 14-28

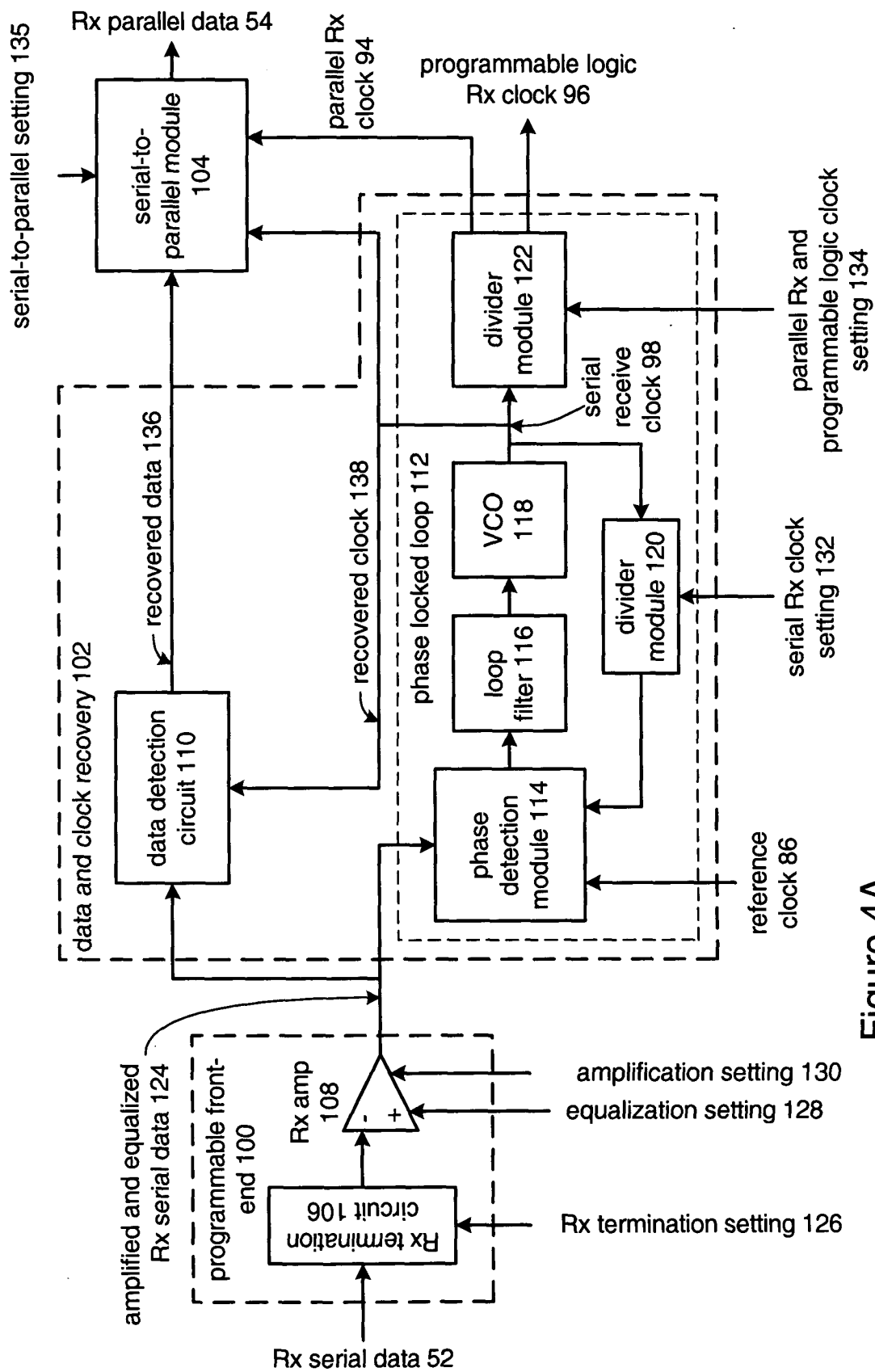


Figure 4A
programmable receive
PMA module 40

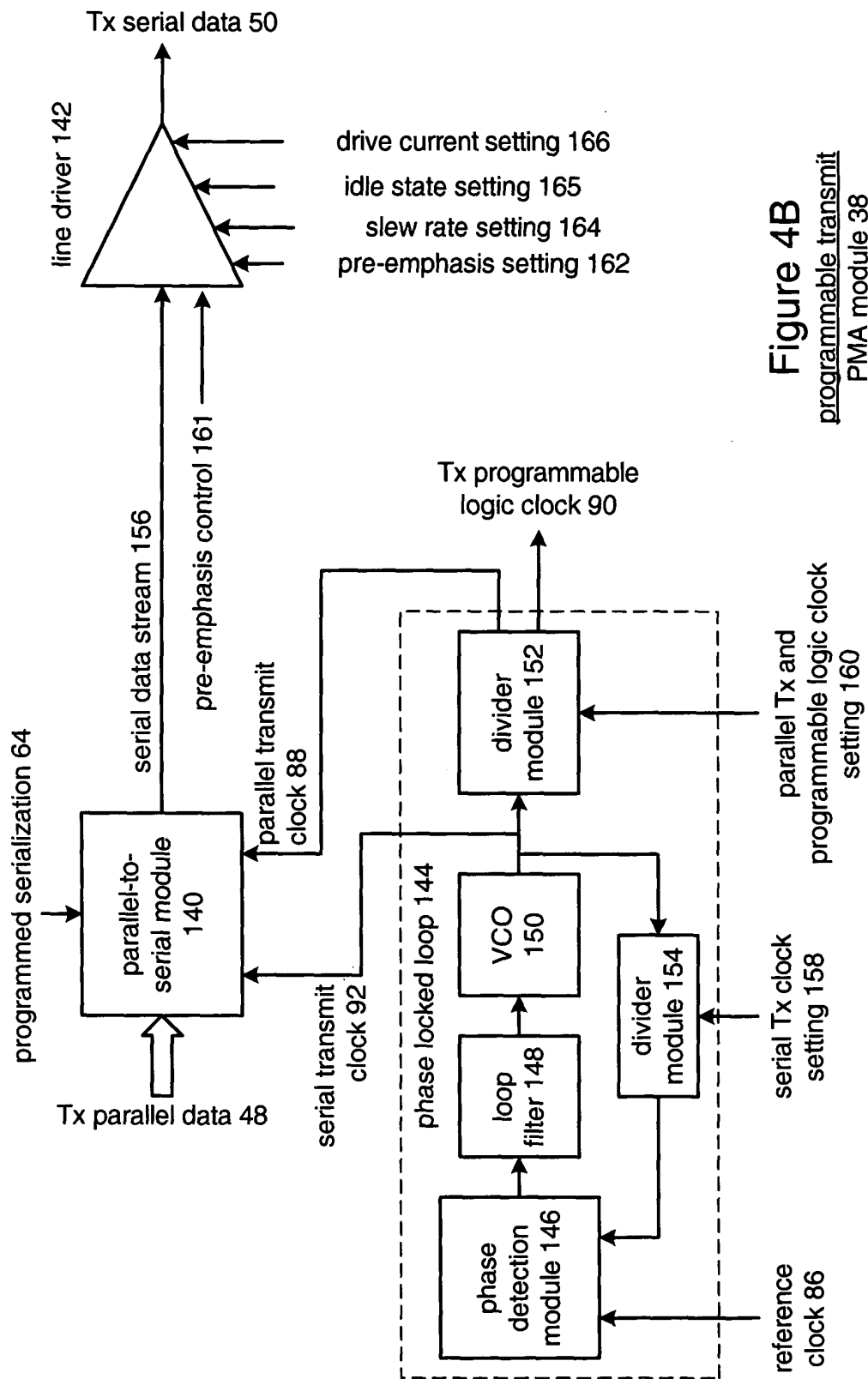


Figure 4B
programmable transmit
PMA module 38

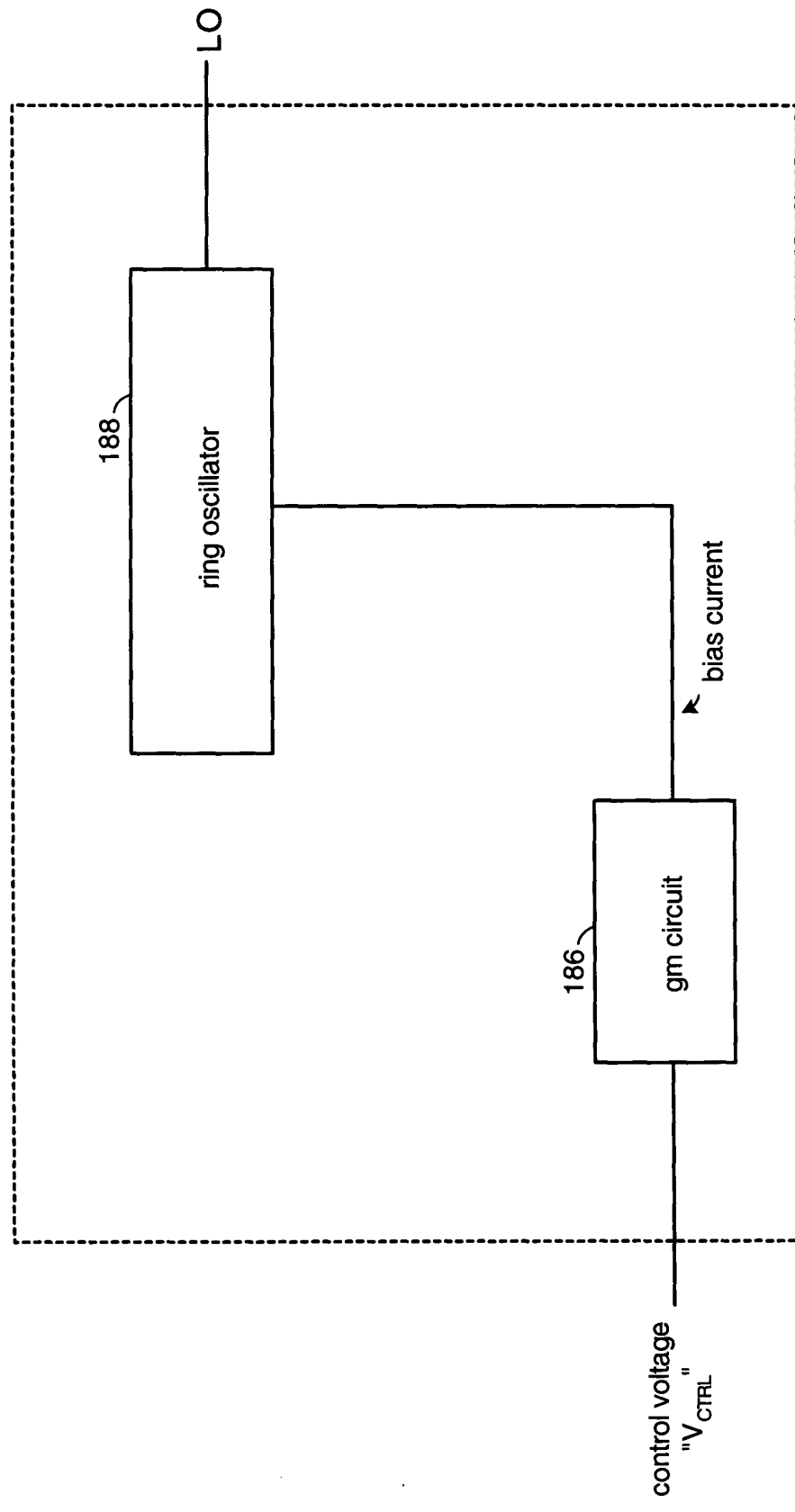


Figure 5 prior art VCO 184

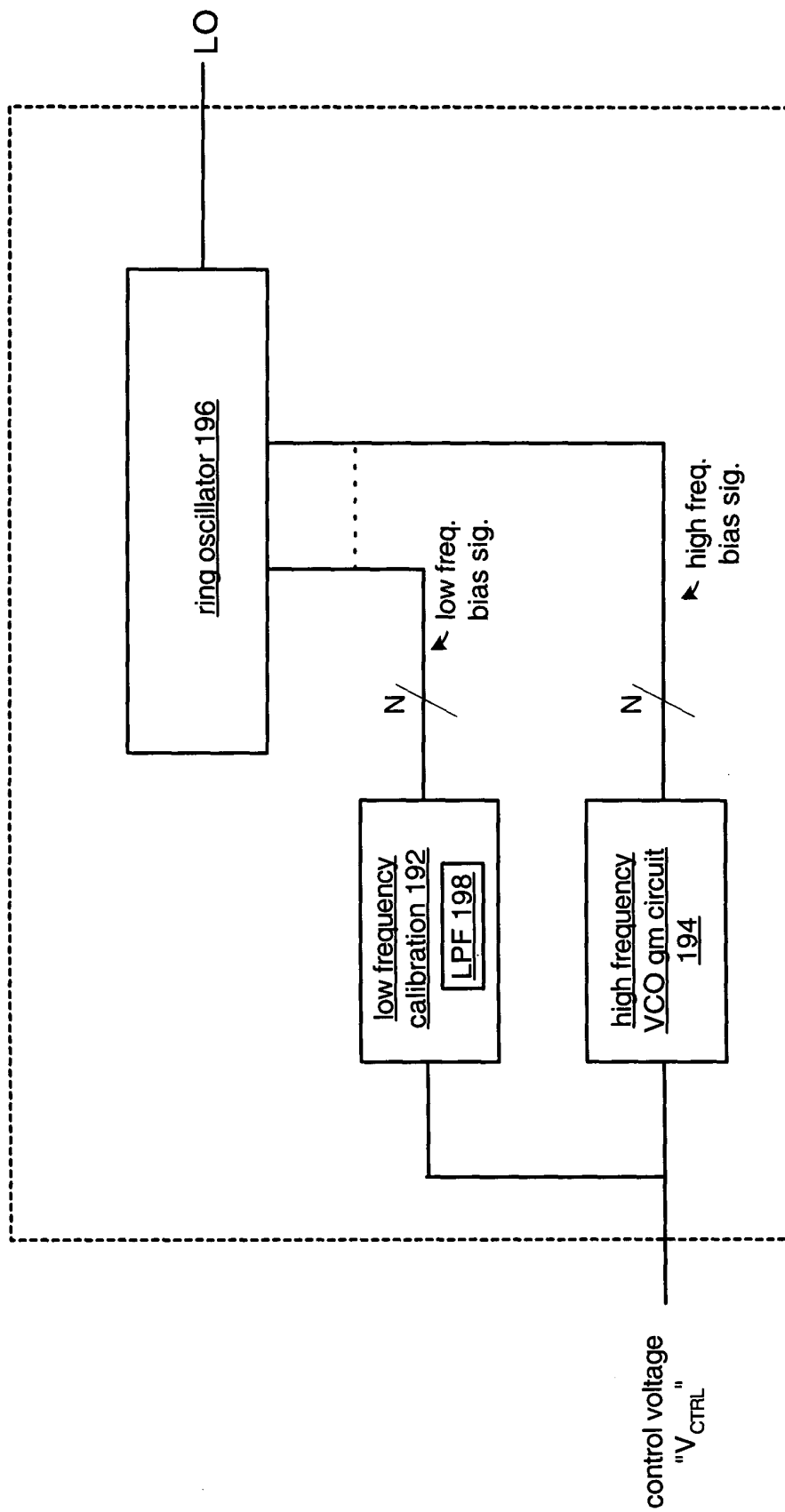


Figure 6 calibrated
VCO 190

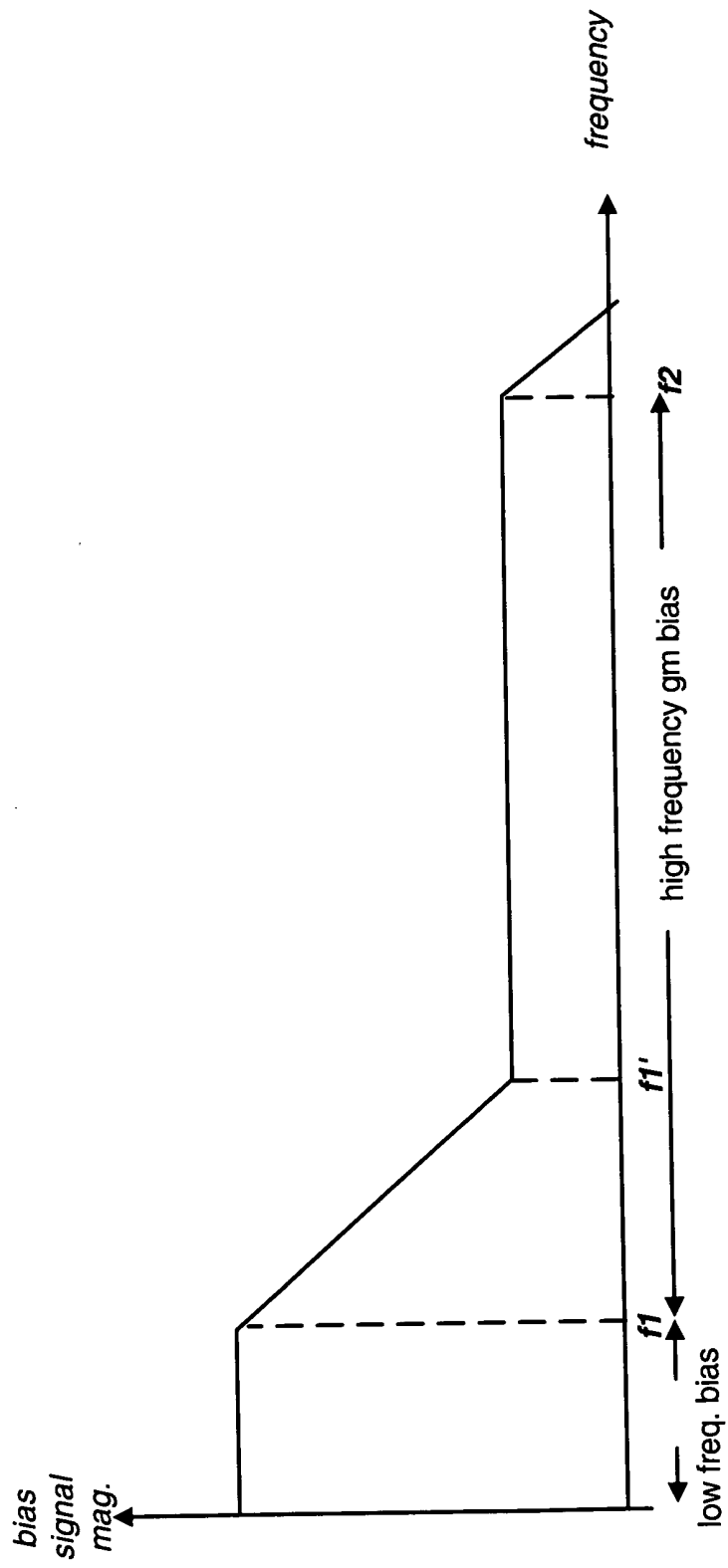


Figure 7 calibrated
VCO frequency
response curve

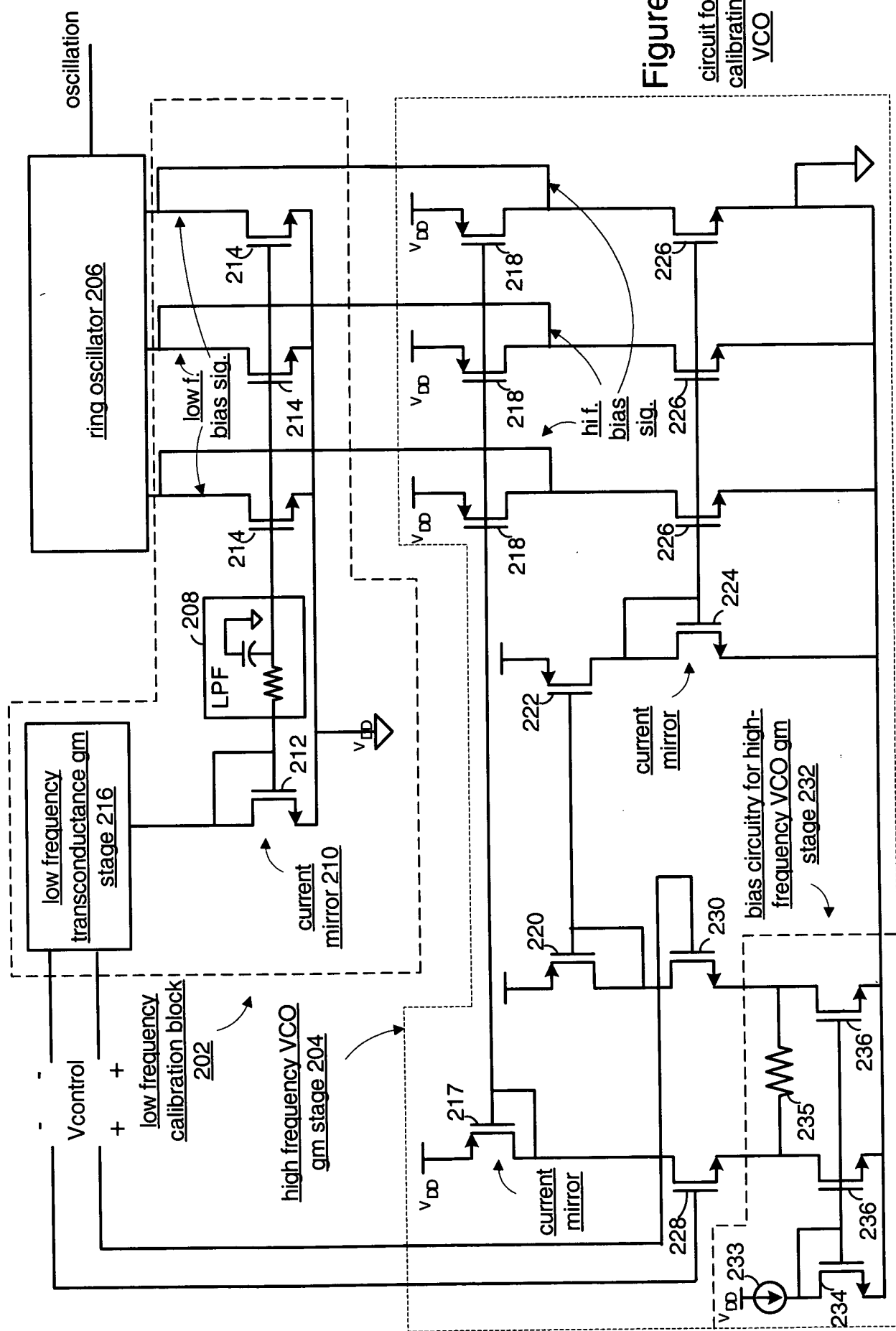
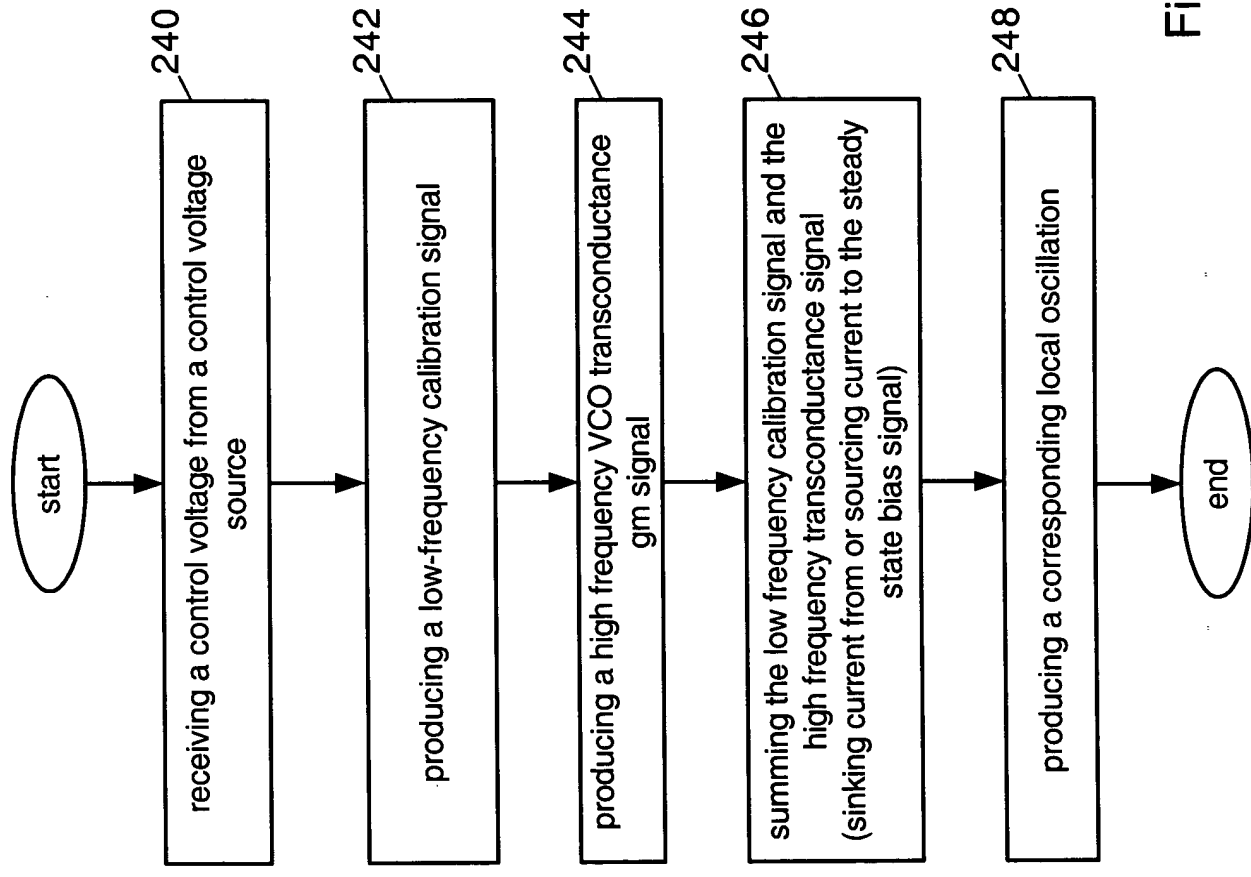


Figure 8
circuit for
calibrating
VCO



method for
calibrating
VCO

Figure 9